

Hybrid - High Reliability Radiation Hardened DC-DC Converter 100V Input, Single and Dual Output

Features

- Total Dose > 100 kRads(Si)
- SEE Hardened to LET up to 82 MeV·cm²/mg
- Low Weight < 110 grams
- Low Input and Output Noise
- Magnetically Coupled Feedback
- 65V to 110V DC Input Range
- Up to 120W Output Power
- Single and Dual Output Models
 Include 3.3, 5, 12, 15, ±5, ±12 and ±15V
- High Efficiency to 87%
- -55°C to +125°C Operating Temperature Range
- $100M\Omega$ @ 500VDC Isolation
- Under-Voltage Lockout
- Short Circuit and Overload Protection
- Adjustable Output with an External Resistor
- Remote Sense on Single Output Models
- Synchronization Input and Output
- External Inhibit
- 3.3 Million-hour MTBF

Potential Applications

- Geostationary Earth Orbit Satellites (GEO)
- Deep Space Satellites / Probes

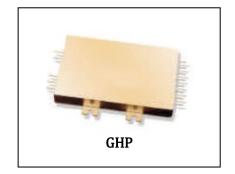
Product Validation

Validated according to MIL-PRF- 38534 for high-reliability applications

PD-97897D

Product Summary

 Part number: GHP10003R3S, GHP10005S, GHP10012S, GHP10015S, GHP10005D, GHP10012D, GHP10015D



100V Input, Single and Dual Output



Ordering Information

Ordering Information

Orderable Part Numbers

Part Number	Package	Screening Level	
GHP10003R3S/CKA	Solder Dipped	Class K	
GHP10003R3S/CKC	Gold Plated	Class K	
GHP10005S/CKA	Solder Dipped	Class K	
GHP10005S/CKC	Gold Plated	Class K	
GHP10012S/CKA	Solder Dipped	Class K	
GHP10012S/CKC	Gold Plated	Class K	
GHP10015S/CKA	Solder Dipped	Class K	
GHP10015S/CKC	Gold Plated	Class K	
GHP10005D/CKA	Solder Dipped	Class K	
GHP10005D/CKC	Gold Plated	Class K	
GHP10012D/CKA	Solder Dipped	Class K	
GHP10012D/CKC	Gold Plated	Class K	
GHP10015D/CKA	Solder Dipped	Class K	
GHP10015D/CKC	Gold Plated	Class K	

Description

The GHP-Series of DC-DC converters are radiation hardened, high reliability converters specifically designed in response to the need for moderate power, high efficiency and well-regulated output required by the modern-day space design applications. Their small size and low weight make them ideal for applications such

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as geostationary earth orbit satellites and deep space probes. They exhibit a high tolerance to total ionizing dose, single event effects and environmental stresses such as temperature extremes, mechanical shock, and vibration.

The converters incorporate a fixed frequency single ended forward topology with magnetic feedback and an internal EMI filter that utilizes multilayer ceramic capacitors that are subjected to extensive lot screening for optimum reliability. By using two stage filtering these converters produce low input and output noise. External inhibit and synchronization input and output allow these converters to be easily incorporated into larger power systems. They are enclosed in a hermetic 3" x 2" x 0.475" package constructed of an Aluminum-Silicon-Carbide (AlSiC) base and an Alloy 48 ring frame and they weigh less than 110 grams. The package utilizes rugged ceramic feed-through copper core pins and is sealed using parallel seam welding.

Manufactured in a facility fully qualified to MIL-PRF-38534, these converters are fabricated utilizing DLA Land and Maritime qualified processes. For available screening options, refer to device screening table in the data sheet

Non-flight versions of the GHP-Series converters are available for system development purposes. Variations in electrical specifications and screening to meet custom requirements can be accommodated

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Specification and Electrical Performance

1 Specification and Electrical Performance

1.1 Maximum and Operating Table

 Table 1
 Absolute Maximum Rating and Recommended Operating Conditions

Absolute Maximum Rating		Recommended Operating Conditions		
Input voltage	-0.5V _{DC} to +120V _{DC}	Input voltage ¹	+65V _{DC} to +110V _{DC}	
Output power	Internally limited	Output power	0 to Max. Rated	
Lead Temperature	+300°C for 10 seconds	Operating Temperature	-55°C to +85°C	
Operating Temperature	-55°C to +125°C			
Storage Temperature	-55°C to +125°C			

1.2 Electrical Performance Characteristics

 Table 2
 Electrical Characteristics

		Conditions		Limits		
Parameter	Group A Subgroups	$-55^{\circ}\text{C} \leq T_{\text{C}} \leq +85^{\circ}\text{C}$ $V_{\text{IN}} = 100\text{V DC} \pm 5\%, C_{\text{L}} = 0\text{uF}$ unless otherwise specified	Min	Nom	Max	Unit
Input voltage (V _{IN})		umess outer wise specified	65	100	110	V
Output Voltage (Vout) GHP10003R3S GHP10005S GHP10012S GHP10015S GHP10015D GHP10015D GHP10015D GHP10005S GHP10005S GHP10015S GHP10015S GHP10015S GHP10015D GHP10015D	1 1 1 1 1 1 2,3 2,3 2,3 2,3 2,3 2,3 2,3 2,3 2,3	$I_{\text{OUT}} = 100\%$ rated load Note 4 $I_{\text{OUT}} = 100\%$ rated load Note 4	3.28 4.98 11.95 14.94 ±4.95 ±11.95 ±14.94 3.24 4.93 11.84 14.80 ±4.90 ±11.84 ±14.80	3.30 5.00 12.0 15.0 ±5.0 0 ±12. 0 ±15.	3.32 5.02 12.05 15.06 ±5.05 ±12.0 5 ±15.0 6 3.36 5.07 12.16 15.20 ±5.10 ±12.1 6 ±15.2 0	V

For Notes to Electrical Performance Characteristics, refer to page 9

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¹ Meets de-rating per MIL-STD-975

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Specification and Electrical Performance

		Conditions		Limits			
Parameter	Group A Subgroups	$-55^{\circ}\text{C} \le T_{\text{C}} \le +85^{\circ}\text{C}$ $V_{\text{IN}} = 100\text{V DC} \pm 5\%, C_{\text{L}} = 0\text{uF}$ unless otherwise specified	Min	Nom	Max	Unit	
		•				I	
Output power (P _{OUT})]	
GHP10003R3S			0		66		
GHP10005S			0		100		
GHP10012S	1,2,3	$V_{IN} = 65, 100, 110 \text{ Volts},$	0		120	147	
GHP10015S		Note 2	0		120	W	
GHP10005D			0		100		
GHP10012D			0		100		
GHP10015D			0		100		
Output current (I _{OUT})							
GHP10003R3S		$V_{IN} = 65, 100, 110 \text{ Volts},$	0		20		
GHP10005S		Note 2	0		20		
GHP10012S			0		10	_	
GHP10015S	1,2,3		0		8.0	A	
GHP10005D		Either Output, Note 3	3.2		16		
GHP10012D		Either Output, Note 3	1.6		6.67		
GHP10015D		Either Output, Note 3	1.3		5.3		
		$V_{IN} = 65, 100, 110 \text{ Volts},$					
Line regulation (VR _{LINE})	1,2,3	$I_{OUT} = 0,50\%, 100\%$ rated,	-10		-10	mV	
	1,2,0	Note 4	-10		-10	111 V	
		$I_{OUT} = 0,50\%,100\%$ rated,					
Load regulation (VR _{LOAD})	1,2,3	Note 4	-0.5		0.5	%	
Load regulation (VINLOAD)	1,2,3	$V_{IN} = 65, 100, 110 \text{ Volts},$	0.5		0.5	70	
Cross regulation (VR _{CROSS})		Duals only, Note 5					
GHP10005D	1,2,3	$V_{IN} = 65, 100, 110 \text{ Volts},$	-5.0		5.0	%	
GHP10012D		V _{IN} = 03, 100, 110 voits,	-3.0		3.0		
GHP10015D			-3.0		3.0		
Total regulation		$V_{IN} = 65, 100, 110 \text{ Volts},$					
(Line, Load, and		$I_{OUT} = 0,50\%,100\%$ rated,					
Temperature)	1,2,3	Dual Model is measured	-2.0		2.0	%	
		From +Output to -Output,					
		Note 14					
Input gurrant (I.)	1,2,3	$I_{OUT} = 0$, Pin 3 open		70	100	m 1	
Input current (I _{IN})		Pin 3 shorted to Pin 2		2.5	5.0	mA	

For Notes to Electrical Performance Characteristics, refer to page 9

100V Input, Single and Dual Output



Specification and Electrical Performance

		Conditions		Limits		
Parameter	Parameter		Min	Nom	Max	Unit
Output ripple (V _{RIP}) GHP10003R3S GHP10005S GHP10012S GHP10015S GHP10005D GHP10012D GHP10015D	1,2,3	$V_{\text{IN}} = 65, 100, 110 \text{ Volts},$ $I_{\text{OUT}} = 100\% \text{ rated load}$ Notes 4, 6		10 15 25 25 20 20 20	50 50 60 60 60 60	mV _{p-p}
Input ripple current	1,2,3	$I_{OUT} = 100\%$ rated load, Note 1		7.0	18	mArm s
Switching frequency (F _S)	1,2,3	Sync. Input (Pin 4) open	450	500	550	kHz
Efficiency (E _{FF}) GHP10003R3S GHP10005S GHP10012S GHP10015S GHP10005D GHP10012D GHP10015D	1,2,3	I _{OUT} = 100% rated load Note 4	68 78 81 82 78 81 82	73 82 85 86 82 84 85		%
Inhibit Input Converter Off Sink current Converter On Sink current	1,2,3	Logic Low on Pin 3 Note 1 Logic High on Pin 3, Note 3 Note 1	-0.5 2.0		0.8 100 50 100	V μA V μA
Synchronization Input frequency range pulse high level pulse low level pulse transition time pulse duty cycle		Ext. Clock on Sync. Input (Pin 4), Note 1	450 4.0 -0.5 40 20		600 10 0.5	kHz V V V/μs %
Current Limit Point Expressed as a percentage of full rated load current	1,2,3	$V_{\text{OUT}} = 90\%$ of Nominal, Note 4			145	%
Power dissipation, load fault (P _D)	1,2,3	Short Circuit, Overload, Note 8			35	W

For Notes to Electrical Performance Characteristics, refer to page 9

100V Input, Single and Dual Output



Specification and Electrical Performance

		Conditions		Limits		
Parameter	Group A Subgroups	$-55^{\circ}\text{C} \le T_{\text{C}} \le +85^{\circ}\text{C}$ $V_{\text{IN}} = 100\text{V DC} \pm 5\%, C_{\text{L}} = 0\text{uF}$	Min	Nom	Max	Unit
		unless otherwise specified				
Output response to step load changes (V _{TLD}) GHP10003R3S GHP10005S GHP10012S GHP10015S GHP10005D GHP10012D GHP10015D	4,5,6	Half Load to/from Full Load, Notes 4,9	-170 -450 -600 -750 -450 -750		170 450 600 750 450 750	mVpk
Recovery time, step load changes (T_{TLD})	4,5,6	Half Load to/from Full Load, Notes 4,9,10			200	μs
Output response to step line changes (V_{TLN})		$65V$ to/from $110V$ $I_{OUT} = 100\%$ rated load, Notes $1,4,11$	-150		150	mVpk
Recovery time, step line changes (T_{TLN})		$65V$ to/from $110V$ $I_{OUT} = 100\%$ rated load, Notes 1,4,10,11			200	μs
Turn-on Response Overshoot (V _{OS}) Turn-on Delay (T _{DLY})	4,5,6	No Load, Full Load Notes 4,12	1.0		2.0 8.0	% ms
Capacitive Load (C _L) GHP10003R3S GHP10005S GHP10012S GHP10015S GHP10005D GHP10012D GHP10015D		I _{OUT} = 100% rated load No effect on DC performance Notes 1,4,7 Each output on duals			6000 5000 1000 1000 1000 500	μF
Line Rejection		$I_{OUT} = 100\%$ rated load DC to 50kHz, Notes 1, 4	40			dB
Isolation	1	Input to Output or Any Pin to Case except Pin 6, test @ 500VDC	100			ΜΩ
Device Weight					110	g
MTBF		MIL-HDBK-217F2, SF, 35°C	3.3 x 10 ⁶			Hrs

For Notes to Electrical Performance Characteristics, refer to page $9\,$

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Notes: Electrical Performance Characteristics Table

Notes: Electrical Performance Characteristics Table

- 1. Parameter is tested as part of design characterization or after design changes. Thereafter, parameter shall be guaranteed to the limits specified.
- 2. Parameter verified during line and load regulation tests.
- 3. Output load current must be distributed such that at least 20% of the total load current is being provided by one of the outputs.
- 4. Load current split equally between outputs on dual output models.
- 5. Cross regulation is measured with 20% rated load on output under test while changing the load on the other output from 20% to 80% of rated.
- 6. Guaranteed for a D.C. to 20MHz bandwidth. Tested using a 20kHz to 10MHz bandwidth.
- 7. Capacitive load may be any value from 0 to the maximum limit without compromising dc performance. A capacitive load in excess of the maximum limit may interfere with the proper operation of the converter's overload protection, causing erratic behavior during turn-on.
- 8. Overload power dissipation is defined as the device power dissipation with the load set such that $V_{\text{OUT}} = 90\%$ of nominal.
- 9. Load step transition time $\geq 10 \, \mu s$.
- 10. Recovery time is measured from the initiation of the transient to where V_{OUT} has returned to within \pm 1%
 - of its steady state value.
- 11. Line step transition time \geq 100 µs.
- 12. Turn-on delay time from either a step application of input power or a logic low to a logic high transition on the inhibit pin (pin 3) to the point where $V_{OUT} = 90\%$ of nominal.
- 13. Although operation at temperatures between $+85^{\circ}$ C and $+125^{\circ}$ C is guaranteed, no parametric limits are specified.
- 14. Total regulation at EOL is +/- 3% maximum.

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Block Diagram

2 Block Diagram

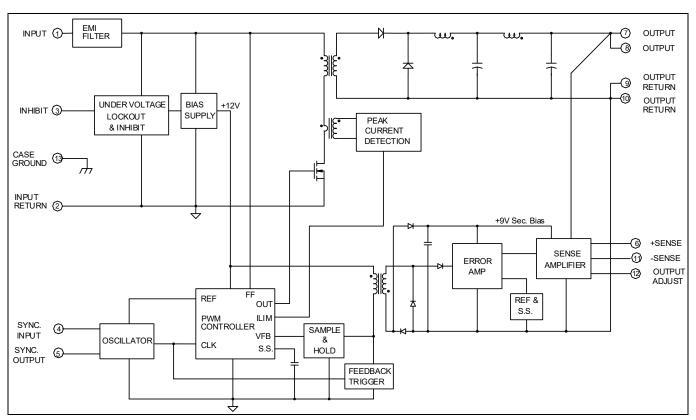


Figure 1 Block Diagram - Single Output

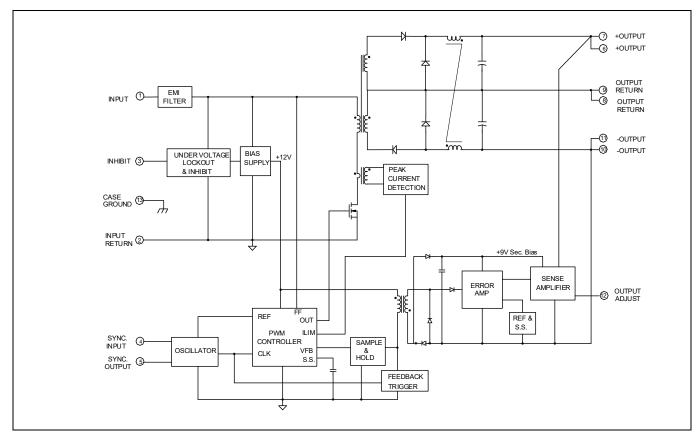


Figure 2 Block Diagram - Dual Output



Radiation Performance table and Application Notes

3 Radiation Performance table and Application Notes

3.1 Radiation Performance table

Table 3 Radiation Performance Characteristics

Test	Conditions	Min	Тур	Unit
Total Ionizing Dose (Gamma)	MIL-STD-883, Method 1019 Operating bias applied during exposure, Full Rated Load, $V_{\text{IN}} = 100\text{V}$	100	150	kRads (Si)
Single Event Effects SEU, SEL, SEGR, SEB	Heavy ions (LET) Operating bias applied during exposure, Full Rated Load, $V_{IN} = 65, 100, 110V$ Test Lab: Texas A & M University	82		MeV·cm²/mg

3.2 Application Notes

A) Attachment of the Converter:

The following procedure is recommended for mounting the converter for optimum cooling and to circumvent any potential damage to the converter.

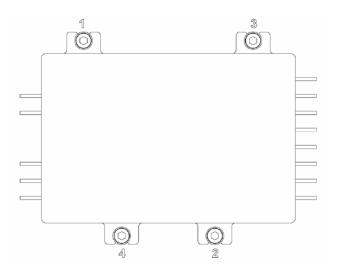
Ensure that flatness of the plate where GHP converter to be mounted is no greater than 0.003" per linear inch. It is recommended that a thermally conductive gasket is used to promote the thermal transfer and to fill any voids existing between the two surfaces. IR HiRel recommends Sil-Pad 2000 with the thickness of 0.010". The shape of the gasket should match the footprint of the converter including the mounting flanges. The gasket is available from IR HiRel. The GHP-Series converter requires either M3 or 4-40 size screws of attachment purposes.

The procedure for mounting the converter is as follows:

- 1. Check the mounting surfaces and remove foreign material, burrs if any or anything that may interfere with the attachment of the converter.
- 2. Place the gasket on the surface reserved for the converter and line it up with the mounting holes.
- 3. Place the converter on the gasket and line both up with mounting holes.
- 4. Install screws using appropriate washers and tighten by hand (\sim 4 in·oz) in the sequence shown below.



Radiation Performance table and Application Notes



5. Tighten the screws with an appropriate torque driver. Torque the screws up to 6 in lb in the sequence shown above

B) Output Voltage Adjustment

Single Output:

To adjust the output voltage of the single output models, a resistor (R_{ADJ}) is connected between the Adjust pin (Pin 12) and either the positive or negative remote sense pins, depending on whether the output voltage is to be adjusted higher or lower than the nominal set-point. This allows the outputs to be reliably adjusted by approximately +10% to -20% of the nominal output voltage. Refer to Fig. 3 and use equations provided to calculate the required resistance (RA_{DJ}).

Note: The output voltage adjust equation does not work as described for the 3.3V Single model. The adjust range for 3.3V model is limited to 3.252V to 3.460V.

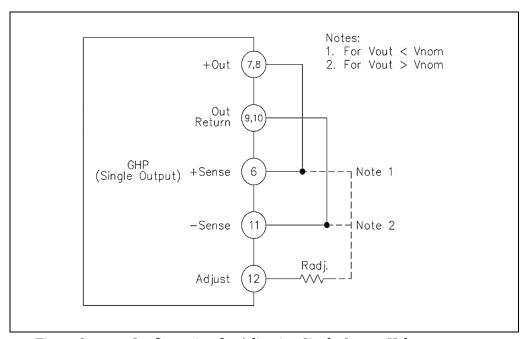


Figure 3 Configuration for Adjusting Single Output Voltage

100V Input, Single and Dual Output



Radiation Performance table and Application Notes

For all Single Output Models, to adjust the output voltages higher:

$$RADJ = \frac{10 \times (VNOM - \Box \Box)}{VOUT - VNOM} - 50$$

To adjust the : R_{ADJ} is in kOhms

 R_{ADI} is connected to the -Out pin and $V_{NOM} < V_{OUT} < 1.1 V_{NOM}$ (Fig. 3, Note 2)

V_{NOM} is the nominal output voltage with the Adjust Pin left open

V_{OUT} is the desired output voltage

For **all Single Output Models,** to adjust the output voltages lower:

$$RADJ = \frac{4 \times (VNOM - \Box x (VOUT - \Box \Box)}{VNOM - VOUT} - 50$$

To adjust the: R_{ADJ} is in kOhms

 R_{ADJ} is connected to the +Out pin and $0.8V_{NOM} < V_{OUT} < V_{NOM}$ (Fig.3, Note 1)

V_{NOM} is the nominal output voltage with the Adjust Pin left open

V_{OUT} is the desired output voltage

Dual Output:

To adjust the output voltage of the dual output models, a resistor (R_{ADJ}) is connected between the Adjust pin (Pin 8) and either output. This allows the outputs to be reliably adjusted by approximately +10% to -20% of the nominal output voltage. Refer to Fig. 4 and use equations provided to calculate the required resistance (R_{ADJ}) .

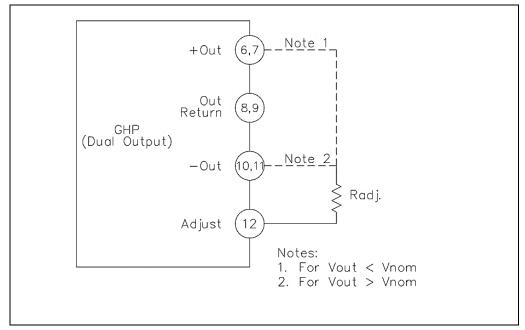


Figure 4 Configuration for Adjusting Dual Output Voltage

For all **Dual Output Models**, to adjust the output voltages higher:

100V Input, Single and Dual Output



Radiation Performance table and Application Notes

$$RADJ = \frac{10 \times (VNOM - 1)}{VOUT - VNOM} - 75$$

To adjust the: R_{ADJ} is in kOhms

 R_{ADJ} is connected to the -Out pin and $V_{NOM} < V_{OUT} < 1.1 V_{NOM}$ (Fig. 4, Note 2)

V_{NOM} is the nominal magnitude of the output voltages with the Adjust pin left open

 V_{OUT} is the desired magnitude of the output voltages

For all **Dual Output Models**, to adjust the output voltages lower:

$$RADJ = \frac{8 \times (VNOM - \Box \Box \times (VOUT - \Box \Box \Box)}{VNOM - VOUT} - 75$$

To adjust the: R_{ADJ} is in kOhms

 R_{ADJ} is connected to the +Out pin and $0.8V_{NOM} < V_{OUT} < V_{NOM}$ (Fig. 4, Note 1)

V_{NOM} is the nominal magnitude of the output voltages with the Adjust pin left open

V_{OUT} is the desired magnitude of the output voltages



4 Mechanical Outlines

Note: For the most updated package outline, please see the website: **GHP-SERIES**

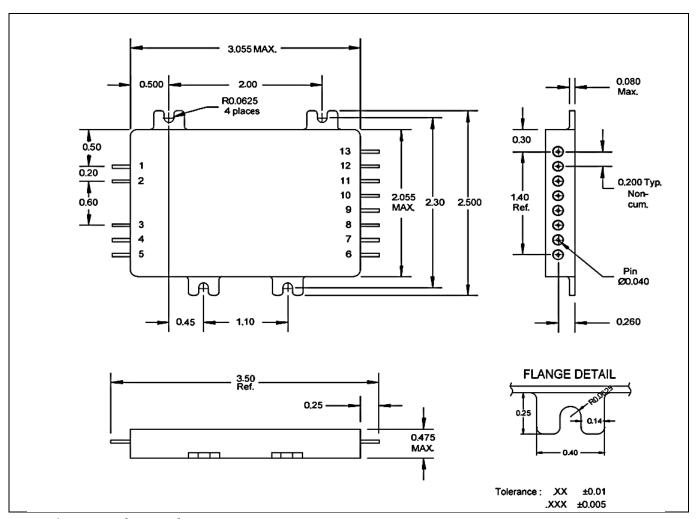


Figure 5 Package outline



Pin Designation

5 Pin Designation

5.1 Pin Designation (Single / Dual)

Table 4 Designation

	Single Output		Dual Output
Pin Number	Designation	Pin Number	Designation
1	Input	1	Input
2	Input Return	2	Input Return
3	Inhibit	3	Inhibit
4	4 Sync. Input 4		Sync. Input
5	5 Sync. Output 5		Sync. Output
6	6 +Sense 6		+ Output
7	7 Output 7		+ Output
8	8 Output 8 Ou		Output Return
9	Output Return	9	Output Return
10	10 Output Return		-Output
11	11 -Sense 11 -Output		-Output
12	Output Adjust	12	Output Adjust
13	Case Ground 13 Case Groun		Case Ground

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Devices Screening

6 Devices Screening

Part Number Designator		/EM①	/CK@
Compliance Level	MIL-PRF-38534	_	K level compliant
Certification Mark		_	СК
Screening Requirement	MIL-STD-883 Method	_	_
Temperature Range	_	Room Temperature	-55°C to +85°C
Element Evaluation	MIL-PRF-38534	N/A	Class K
Non-Destructive Bond Pull	2023	N/A	Yes
Internal Visual	2017	IR Defined	Yes
Temperature Cycle	1010	N/A	Cond C
Constant Acceleration	2001, Y1 Axis	N/A	3000 Gs
PIND	2020	N/A	Cond A
Burn-In	1015	N/A	320 hrs @ 125°C (2 x 160 hrs)
Final Electrical	MIL-PRF-38534	Doom Town orduna	-55°C, +25°C,
(Group A)	& Specification	Room Temperature	+85°C
PDA	MIL-PRF-38534	N/A	2%
Seal, Fine and Gross	1014	N/A	Cond CH
Radiographic	2012	N/A	Yes
External Visual	2009	IR Defined	Yes

Notes:

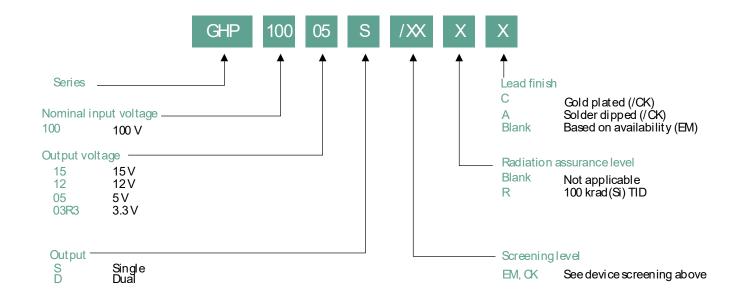
- ① **"EM" grade** parts are strictly intended to permit the customer to determine the electrical functionality of the device in the customer's application in ambient conditions. The use of EM devices in production applications presents an unquantifiable risk of failure and IR HiRel disclaims all responsibility for such failure.
- ² "CK" grade is the flight model (FM) compliant to K Level screening as defined in the DLA Land and Maritime MIL-PRF-38534 requirements, but is not necessarily a DLA Land and Maritime qualified SMD per MIL-PRF-38534. The governing document for this part number designator is the IR HiRel datasheet (this document). Radiation rating as stated in the "Radiation Performance Characteristics" section, is verified by analysis and test per IR HiRel internal procedure. The

part is marked with the IR base part number and the "CK" certification mark.



Part Numbering

Part Numbering



100V Input, Single and Dual Output



Revision history

Revision history

Document version	Date of release	Description of changes
	02/21/2018	Datasheet (PD-97897)
Rev A	02/11/2022	Updated based on ECO-1110_29655
Rev B	04/30/2020	Updated based on ECO-1110_29731
Rev C	08/30/2021	Updated based on ECO-1110_30394
Rev D	01/20/2023	Updated based on ECO-1110_30895



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