DO5 STUD HIGH CURRENT ISOLATED RECTIFIER ASSEMBLY

SET100203
SET100219
SET100212
SET100204
SET100211

HIGH CURRENT, HIGH DENSITY, ISOLATED, SILICON POWER RECTIFIER DO5 STUD

- Low thermal impedance
- Small size and low weight
- High current applications
- Isolated for direct heatsink mounting
- High surge ratings

QUICK REFERENCE DATA

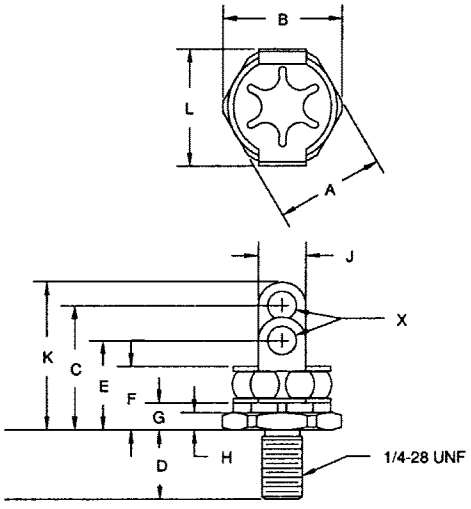
- V_R = 150V - 1000V
- I_F = 90A
- t_{rr} = 30nS - 2µS
- I_{FSM} ≥ 750A

ABSOLUTE MAXIMUM RATINGS

Device Type	Working Reverse Voltage (V_{RWM})	Average Rectified Current ($I_{F(AV)}$) @ T_{mb}			1 Cycle Surge I_{FSM} t_P = 8.3mS		Repetitive Surge (I_{FRM})	Operating & Storage Temperature Range
		@ 55°C	100°C	125°C	@ 25 °C	@ 100°C	@ 25 °C	(T_{OP}) (T_{STG})
	Volts	Amps	Amps	Amps	Amps	Amps	Amps	°C
SET100203	1000	90	66	48	750	600	150	-55 to +175
SET100219	1000	60	48	36	750	480	90	-55 to +175
SET100212	600	90	66	48	750	600	150	-55 to +175
SET100204	400	90	66	48	750	600	150	-55 to +175
SET100211	150	90	60	42	870	750	144	-55 to +150

$R_{\theta JMB}$ = 0.5°C/W for all varieties, other configurations available see next page for details

MECHANICAL



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DIM	DIMENSIONS MM MIN	MM MAX	INCHES MIN	INCHES MAX	NOTE
A	17.0	17.6	.67	.69	-
B	18.7	19.3	.74	.76	-
C	19.5	20.6	.77	.81	-
D	10.6	11.7	.42	.46	-
E	13.9	15.0	.55	.59	-
F	-	11.0	-	.43	-
G	4.3	-	.17	-	-
H	2.5	3.1	.10	.12	-
J	7.3	7.9	.29	.31	-
K	-	25.4	-	1.0	-
L	-	19.1	-	.75	-
X	4.3	4.6	.170	.180	DIA

NOTES:
1. POLARITY - RED DOT DENOTES CATHODE TERM.

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ELECTRICAL CHARACTERISTICS

Device Type	Maximum Leakage Current @ V_{RWM}		Maximum Forward Voltage @ 54.0 A	Maximum Reverse Recovery Time
	T_j = 25 °C	T_j = 100 °C		
	µA	µA	Volts	nS
SET100203	6.0	120	1.2	2000
SET100219	6.0	150	2.2	150
SET100212	6.0	120	1.2	2000
SET100204	6.0	120	1.5	150
SET100211	60.0	3mA	1.1	30

OTHER CONFIGURATIONS

The Part Numbers Shown in this data Sheet are Isolated with the cathode at the stud end of the device. Part numbers for other configurations are shown below:

Isolated Cathode to Stud	Isolated Anode to Stud	Non-Isolated Cathode to Stud	Non-Isolated Anode to Stud
SET100203	SET100403	SET100103	SET100303
SET100219	SET100419	SET100119	SET100319
SET100212	SET100412	SET100112	SET100312
SET100204	SET100404	SET100104	SET100304
SET100211	SET100411	SET100111	SET100311

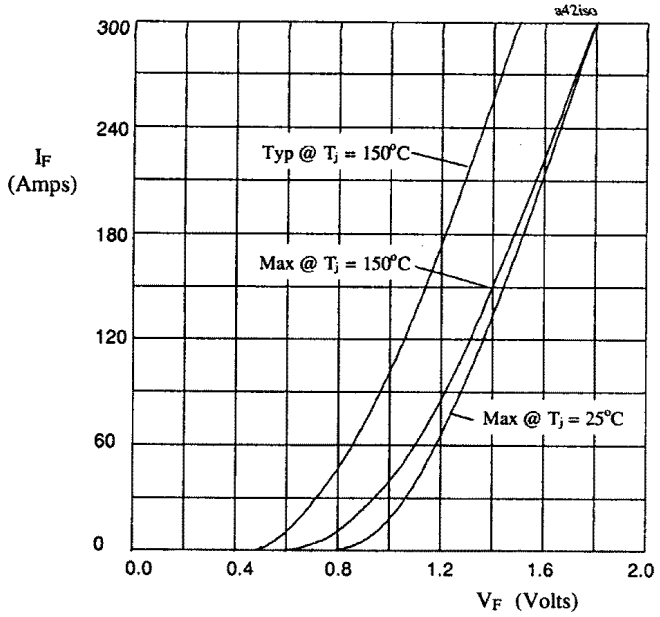

Figure 1. Forward voltage drop as a function of forward current for SET10**03 & SET10**12.

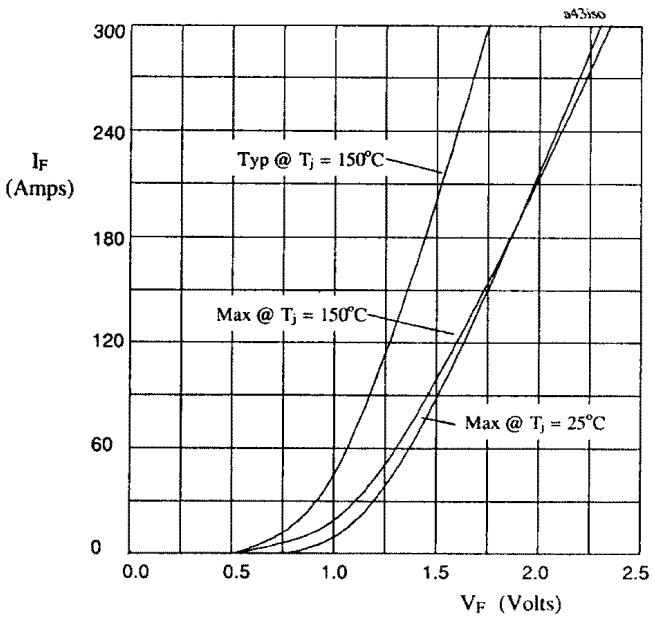

Figure 2. Forward voltage drop as a function of forward current for SET10**04.

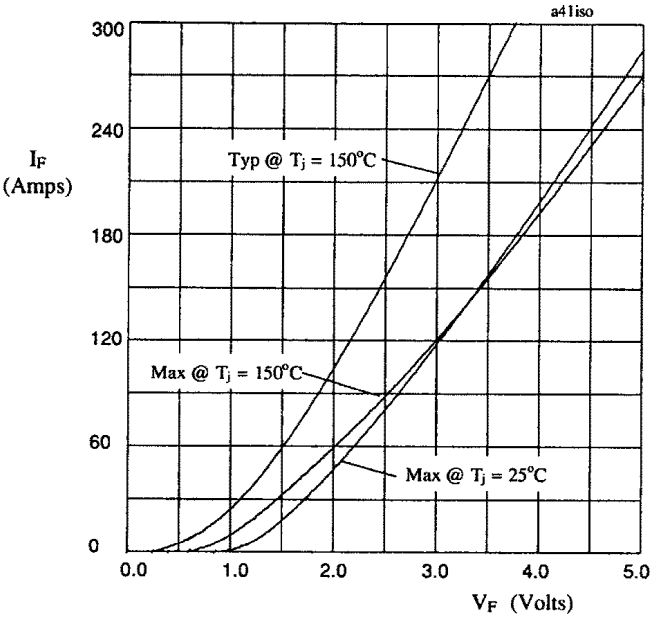

Figure 3. Forward voltage drop as a function of forward current for SET10**19.

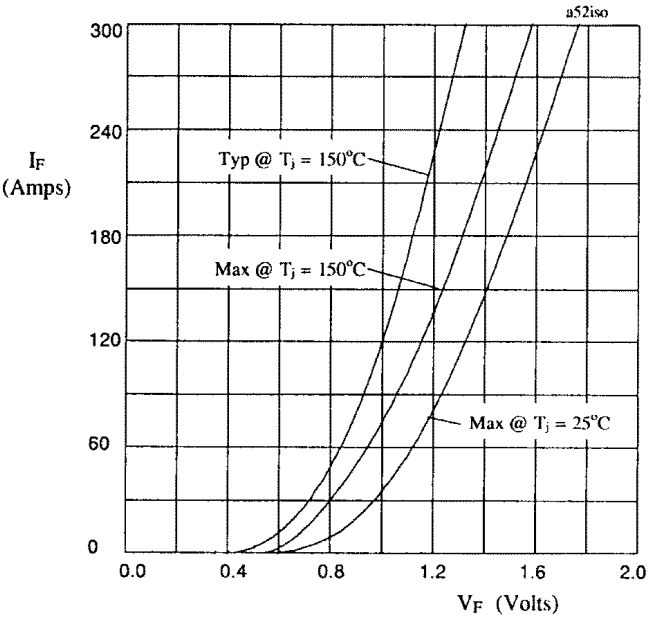

Figure 4. Forward voltage drop as a function of forward current for SET10**11.

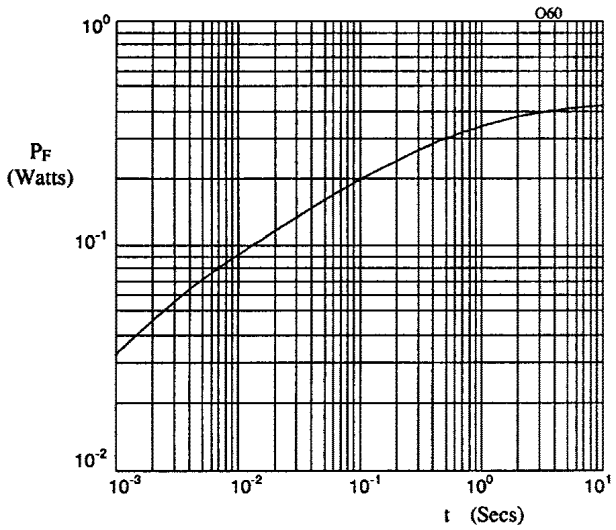

Figure 5. Typical transient thermal impedance characteristic.

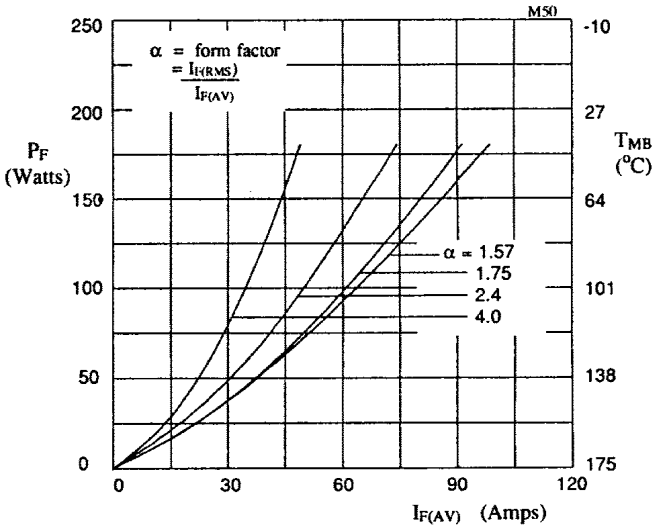

Figure 6. Forward power dissipation and maximum allowable mounting base temperature as a function of forward current for sinusoidal operation, for SET10**03 and SET10**12.

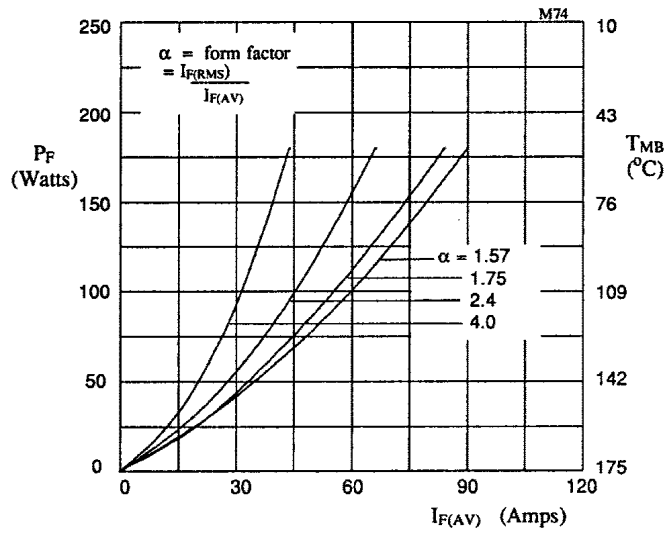

Figure 7. Forward power dissipation and maximum allowable mounting base temperature as a function of forward current for sinusoidal operation, for SET10**04.

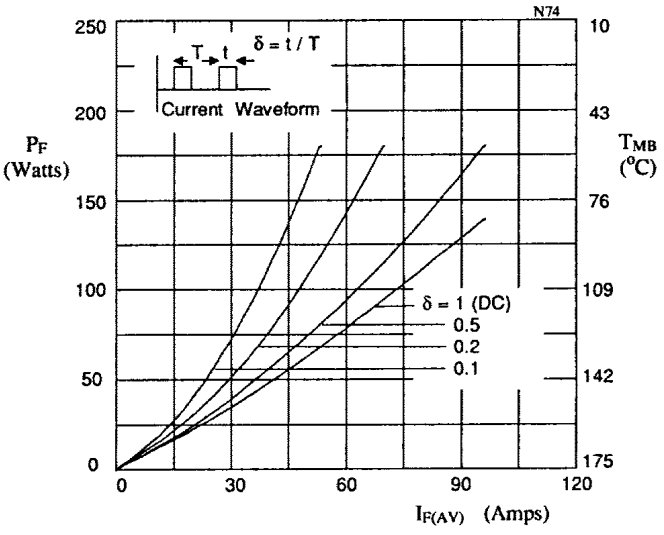

Figure 8. Forward power dissipation and maximum allowable mounting base temperature as a function of forward current for square wave operation, for SET10**04

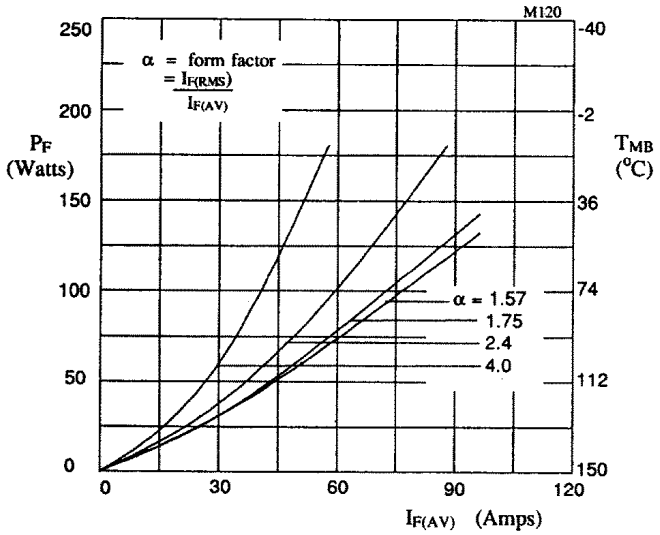

Figure 9. Forward power dissipation and maximum allowable mounting base temperature as a function of forward current for sinusoidal operation, for SET10**11.

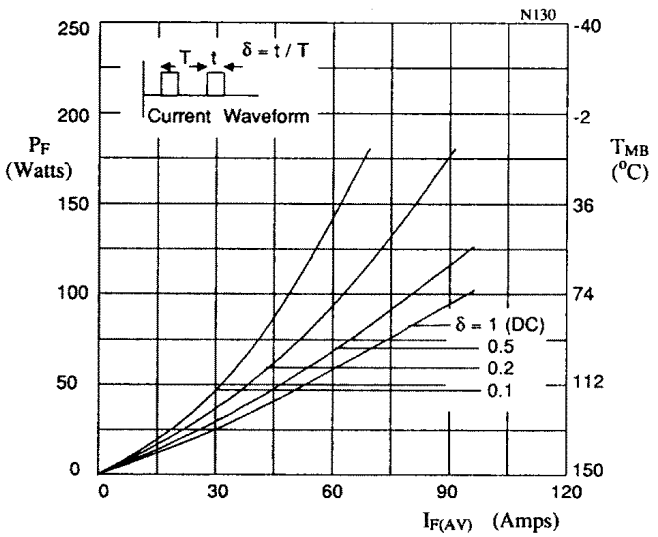

Figure 10. Forward power dissipation and maximum allowable mounting base temperature as a function of forward current for square wave operation, for SET10**11.