

EADP

Micross EADP converters offers state of the art performance and is an ideal centralized power supply for mixed signal applications and onboard processors up to 160W. The EADP includes voltage adjust interfaces and output sequencing which can be tailored for specific equipment requirements.

**RAD-HARD, ITAR FREE**  
**100 kRad and 60 MeV**

## FEATURES

### Electrical Performance

- Centralized EPC for Mixed Signal and Processor Applications
- Two Adjustable High Power Outputs with High Efficiency
- Two Low Power Output with Excellent Noise Performance
- Output Over-Voltage Protection and Sequencing
- Telecommand ON/OFF and Logic Level Enable Input
- WC EOL Output Voltage Accuracy:  $\pm 2\%$  Including Line and Load
- Load Step Transient Response:  $\pm 5\%$  for a 50% to 100% Load Step

### Mechanical

PCB: 171.5mm x 103mm x 27mm < 350g  
Chassis: 171.5mm x 125mm x 31mm < 6300g

### Output CE:

V1 and V2: < 10.0mVrms (50Hz to 50MHz)  
V3 and V4: < 1.0mVrms (50Hz to 50MHz)

### CS Rejection Input to Outputs:

V1 and V2: > 40dB  
V3 and V4: > 85dB

### Output Configurations

Output 1: +2.5V to +20V 15A / 120W max  
Output 2: +2.5V to +20V 15A / 120W max  
Output 3: +2.5V to +15V 3A / 15W max  
Output 4: -2.5V to -15V 1A / 5W max

\*Output 3 and 4 Can Be Stacked for a Single Output Up to 30V

## BENEFITS

### Standard 3U Form-Factor, Tailored to Spec

- Fully Customizable to Match Satellite Platform and Payload Requirements
- Two High Efficiency Main Outputs + Two Low Noise Auxiliary Outputs
- On-Board EMC Filters Ensures Compliance Without Additional Filtering
- Input to Output Power Efficiency of up to 92%
- Design Data Package & Product Control Documentation Available

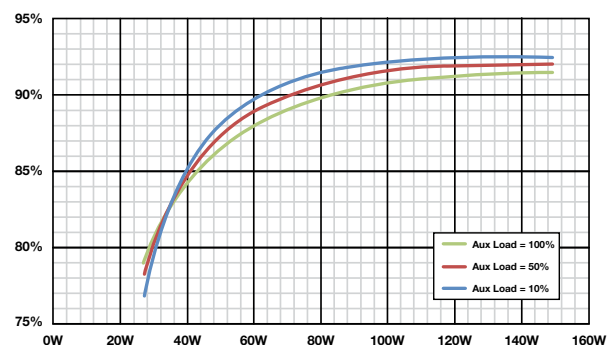
### Design Expertise

Our team helps review and specify payload specific DC-DC converters to ensure maximum compatibility and minimum risk at equipment level. We design, develop, manufacture and test complete DC-DC solutions for effortless payload integration.

### Rapid Delivery for Tailored Designs:

- 6 Months for Engineering Models
- 9 Months for CDR Data Package
- 12 Months for Flight Units

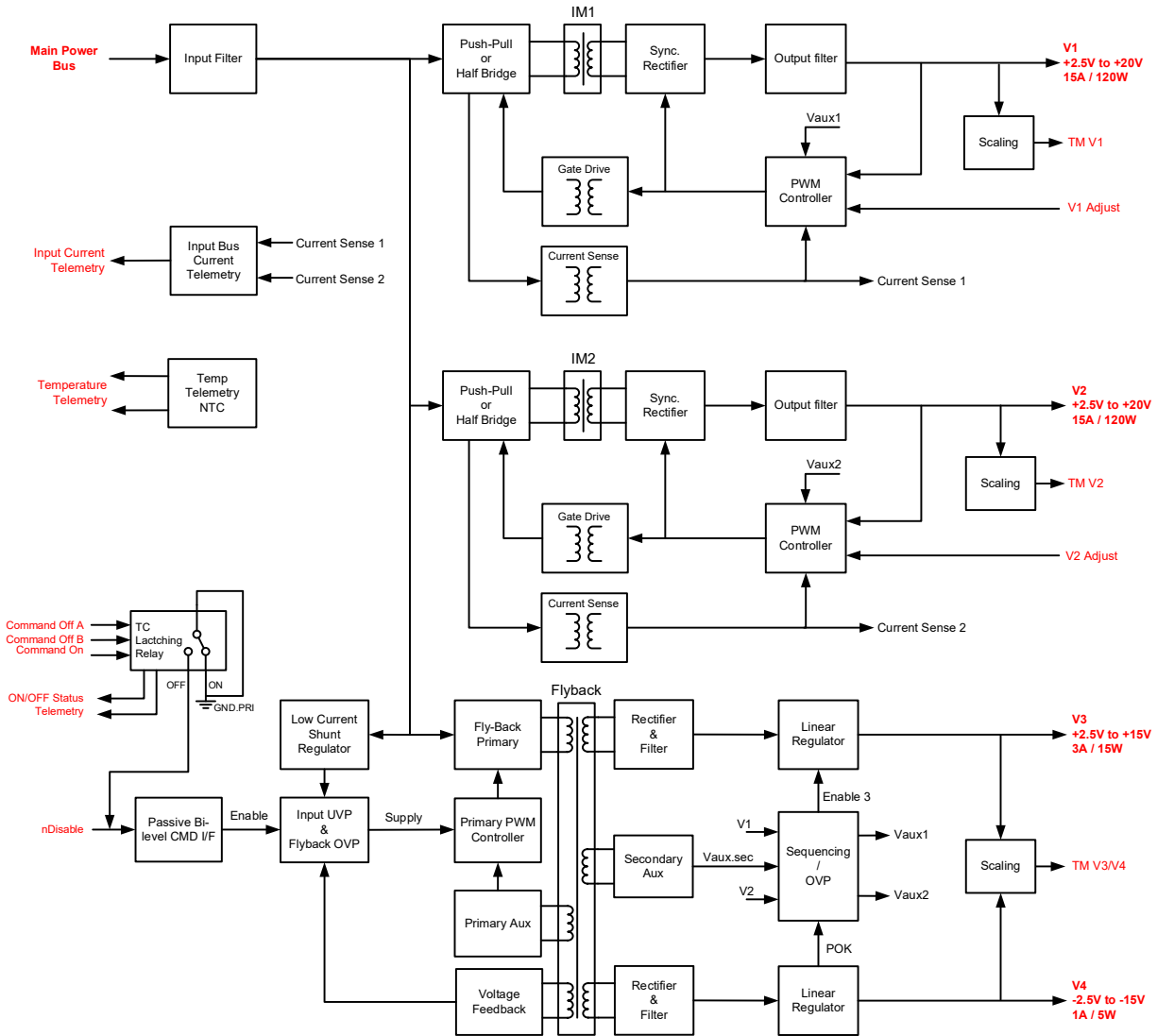
Typical Efficiency



Efficiency vs Output Power, Parametric with Load on Output 3 & 4

# EADP Series

## GENERIC BLOCK SCHEMATIC



### Flight Qualified and Export Approved Configurations

Part Number	Input Voltage	V1	V2	V3	V5
12129	93V - 105V	+5V / 10.0A	+3.3V / 14.0A	V3 and V4 Stacked: +27V / 0.50A	
12178	24V - 34V	+20V / 1.50A	+8.0 / 4.0A	Not Used	-15V / 0.20A

ECCN: 9A515.y.1

### About Micross

Micross is the most complete provider of advanced microelectronic services and component, die and wafer solutions. With the broadest authorized access to die & wafer suppliers, an extensive portfolio of hi-rel power, RF, optoelectronics, memory, data bus, logic, and SMD/5962 qualified products, and the most comprehensive advanced packaging, assembly, modification, upscreening, and test capabilities, Micross is uniquely positioned to provide unparalleled high-reliability solutions, from bare die, to fully packaged devices including hermetic ICs/MCMs, PEMS, ASICs, FPGAs, and PCBs, to complete program life-cycle sustainment. For more than 45 years, Micross has been a trusted source for the aerospace, defense, space, medical, energy, communications, and industrial markets.



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